AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

LISTING OF CLAIMS:

1. (currently amended) A microcircuit ~~Microcircuit~~ card ~~including~~ comprising:

- input-output means ~~(14)~~ for receiving a continuous stream of digital data ~~(DATA)~~;

- processing means ~~(12)~~ for processing said digital data; and

- stream control means ~~(26)~~,

~~the microcircuit card being characterized in that~~ wherein the processing means ~~(12) include~~ comprises:

- direct memory access transfer means ~~(DMA)~~ for transferring said continuous stream of digital data ~~(DATA)~~ between the input-output means ~~(14)~~ and a storage area ~~(18)~~; and

- communication means ~~(20)~~ for communicating with the stream control means ~~(26)~~ security data ~~(DATA_CTRL)~~ obtained from said digital data ~~(DATA)~~,

the stream control means ~~(26)~~ being adapted to control the transfer of the continuous stream of digital data ~~(DATA)~~ by the direct memory access transfer means ~~(DMA)~~ taking into account said security data ~~(DATA_CTRL)~~.

2. (currently amended) The microcircuit ~~Microcircuit~~ card according to claim 1, ~~characterized in that~~ wherein said security data ~~(DATA_CTRL)~~ consists at least in part of a portion of said digital data ~~(DATA)~~.

3. (currently amended) The microcircuit ~~Microcircuit~~ card according to claim 2, ~~characterized in that~~ wherein said security data ~~(DATA_CTRL)~~ includes authentication data ~~(AUTH)~~ for authenticating a portion (P1) of the digital data received by the card, the stream control means ~~(26)~~ being adapted to verify the validity of said digital data ~~(DATA)~~ on the basis of this authentication data ~~(AUTH)~~ and to control said transfer as a function of the result of this verification.

4. (currently amended) The microcircuit ~~Microcircuit~~ card according to claim 1, ~~characterized in that~~ wherein said processing means ~~(12)~~ are adapted to insert into said security data ~~(DATA_CTRL)~~ a result of processing said digital data ~~(DATA)~~.

5. (currently amended) The microcircuit ~~Microcircuit~~ card according to claim 4, ~~characterized in that~~ wherein said processing result is the result of a step of authenticating said digital data.

6. (currently amended) The microcircuit ~~Microcircuit~~ card according to claim 1, ~~characterized in that~~ wherein the stream control means are adapted to modify at least one operating parameter of said direct memory access transfer means ~~(DMA)~~.

7. (currently amended) The microcircuit ~~Microcircuit~~ card according to claim 6, ~~characterized in that~~ wherein said parameter is selected from an address of said storage area ~~(18)~~ and a parameter for selecting a protocol for communication between the input-output means ~~(14)~~ and the storage area ~~(18)~~.

8. (currently amended) The microcircuit ~~Microcircuit~~ card according to claim 1, ~~characterized in that~~ wherein said processing means ~~(12)~~ include a data compression unit ~~(13)~~, a data decompression unit, a data encryption unit or a data decryption unit.

9. (currently amended) The microcircuit ~~Microcircuit~~ card according to claim 1, ~~characterized in that~~ wherein said stream control means ~~(26)~~ are adapted to command stopping of the transfer of the continuous stream of digital data ~~(DATA)~~ by said direct memory access transfer means ~~(DMA)~~ if they detect the presence of invalid authentication data in said digital data ~~(DATA)~~ on the basis of said security data ~~(DATA_CTRL)~~.

10. (currently amended) The microcircuit ~~Microcircuit~~ card according to claim 1, ~~characterized in that~~ wherein the stream control means ~~(26)~~ are further adapted to obtain preliminary data directly from the input-output means ~~(14)~~, the stream control means ~~(26)~~ also taking account of the preliminary data in authorizing or refusing the transfer of the digital data ~~(DATA)~~ by the direct memory access transfer means ~~(DMA)~~.

11. (currently amended) The microcircuit ~~Microcircuit~~ card according to claim 10, ~~characterized in that~~ wherein said preliminary data includes authentication data ~~(PASSWD)~~.

12. (currently amended) The microcircuit ~~Microcircuit~~ card according to claim 10, ~~characterized in that~~ wherein said data includes a storage address for said digital data.

13. (currently amended) The microcircuit ~~Microcircuit~~ card according to claim 1, ~~characterized in that~~ wherein it further includes regulation means ~~(PLL)~~ adapted to modify a clock frequency applied to the processing means ~~(12)~~ as a function of said security data ~~(DATA_CTRL)~~.

14. (cancelled)

15. (currently amended) The microcircuit ~~Microcircuit~~ card according to claim 11, ~~characterized in that~~ wherein said data includes a storage address for said digital data.

16. (currently amended) A microcircuit card ~~including~~ comprising:

- first input-output means for receiving digital data;

- processing means for processing said digital data;

- transfer means for transferring said digital data between the first input-output means and a storage area;

- second input-output means for receiving preliminary data; and

- stream control means adapted to control the transfer of digital data by the transfer means taking into account said preliminary data.

17. (currently amended) The ~~A~~ microcircuit card according to claim 16, wherein said transfer means include a DMA component.